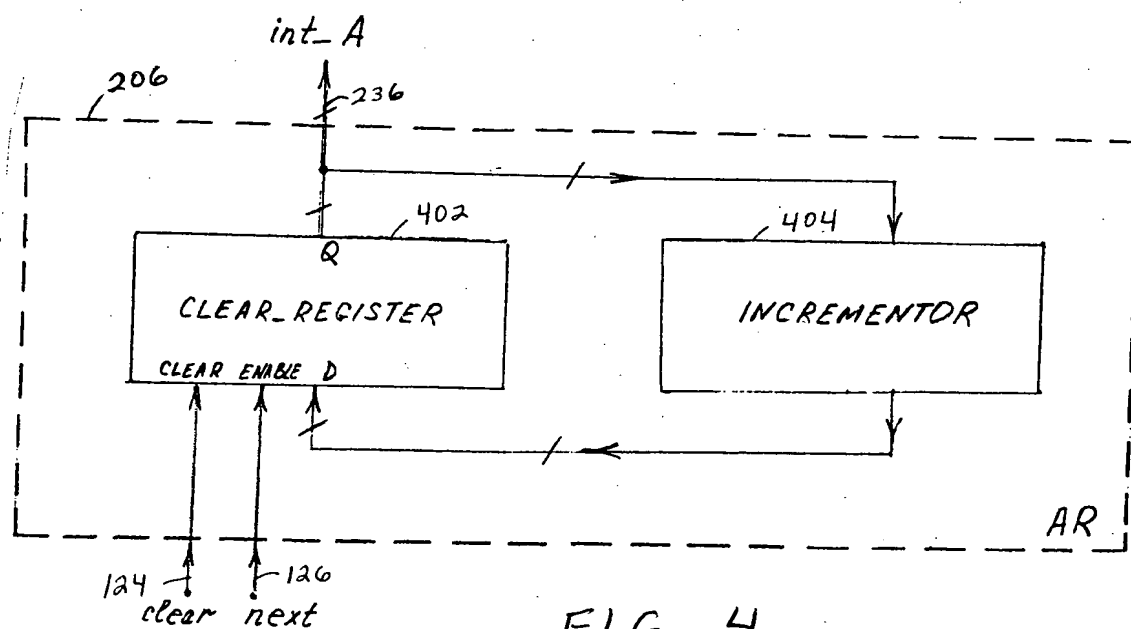
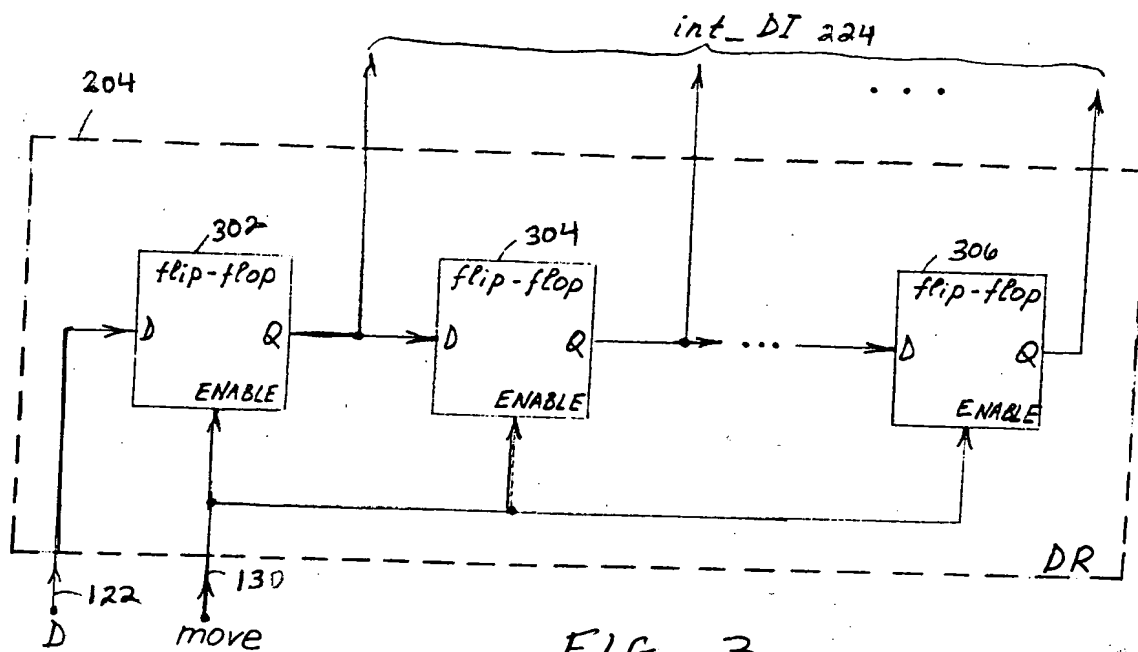


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500

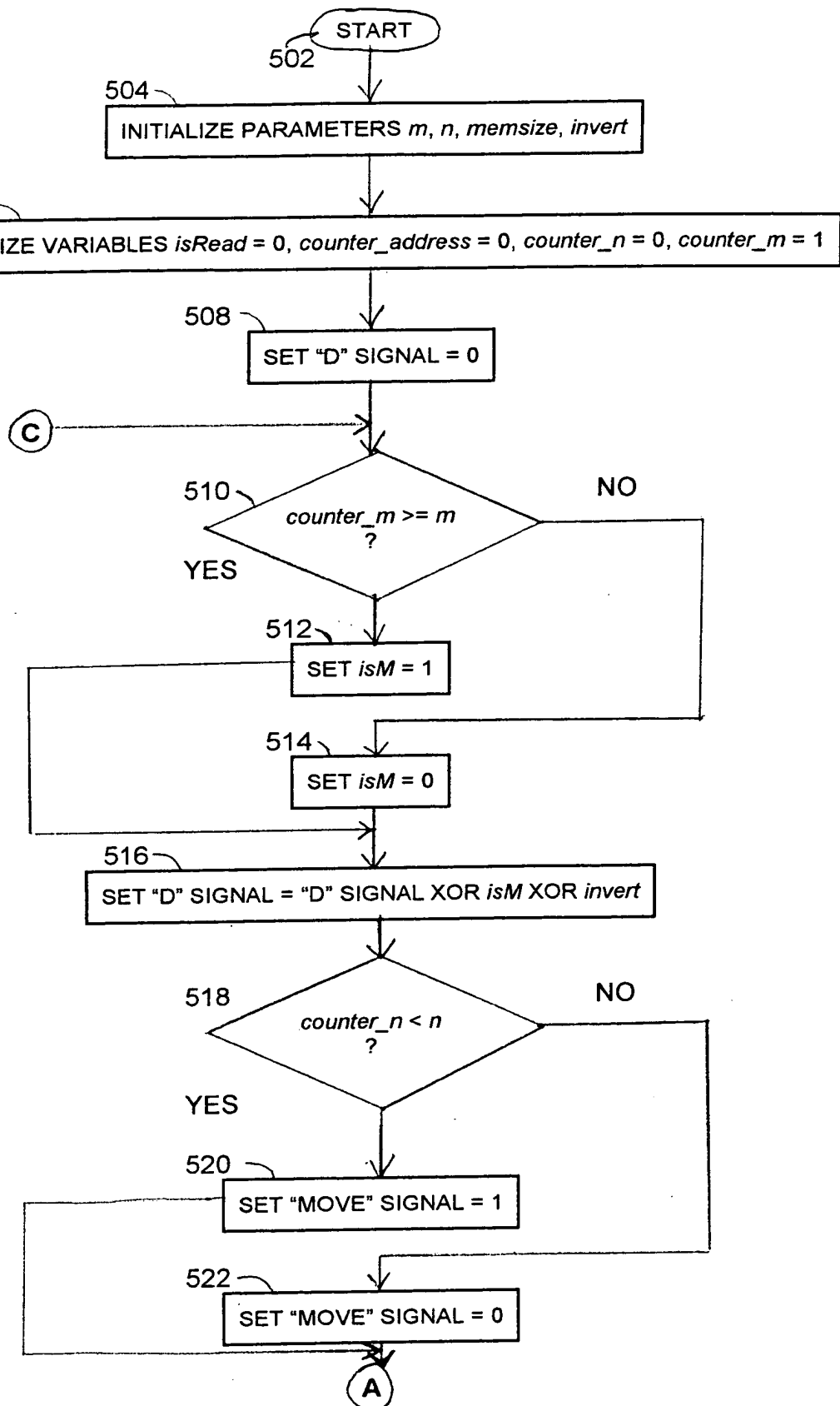


FIG. 5A

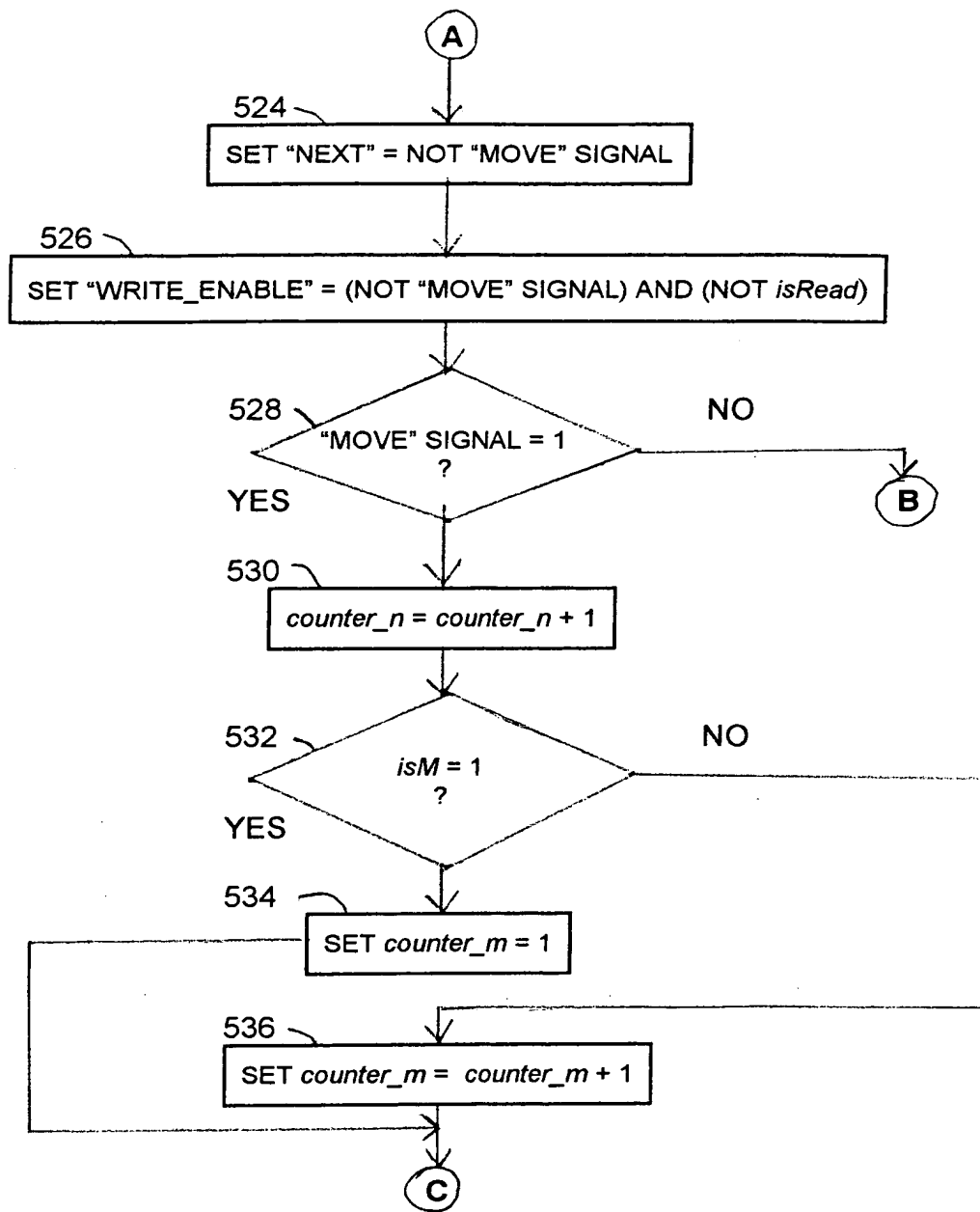


FIG. 5B

10027344 133101

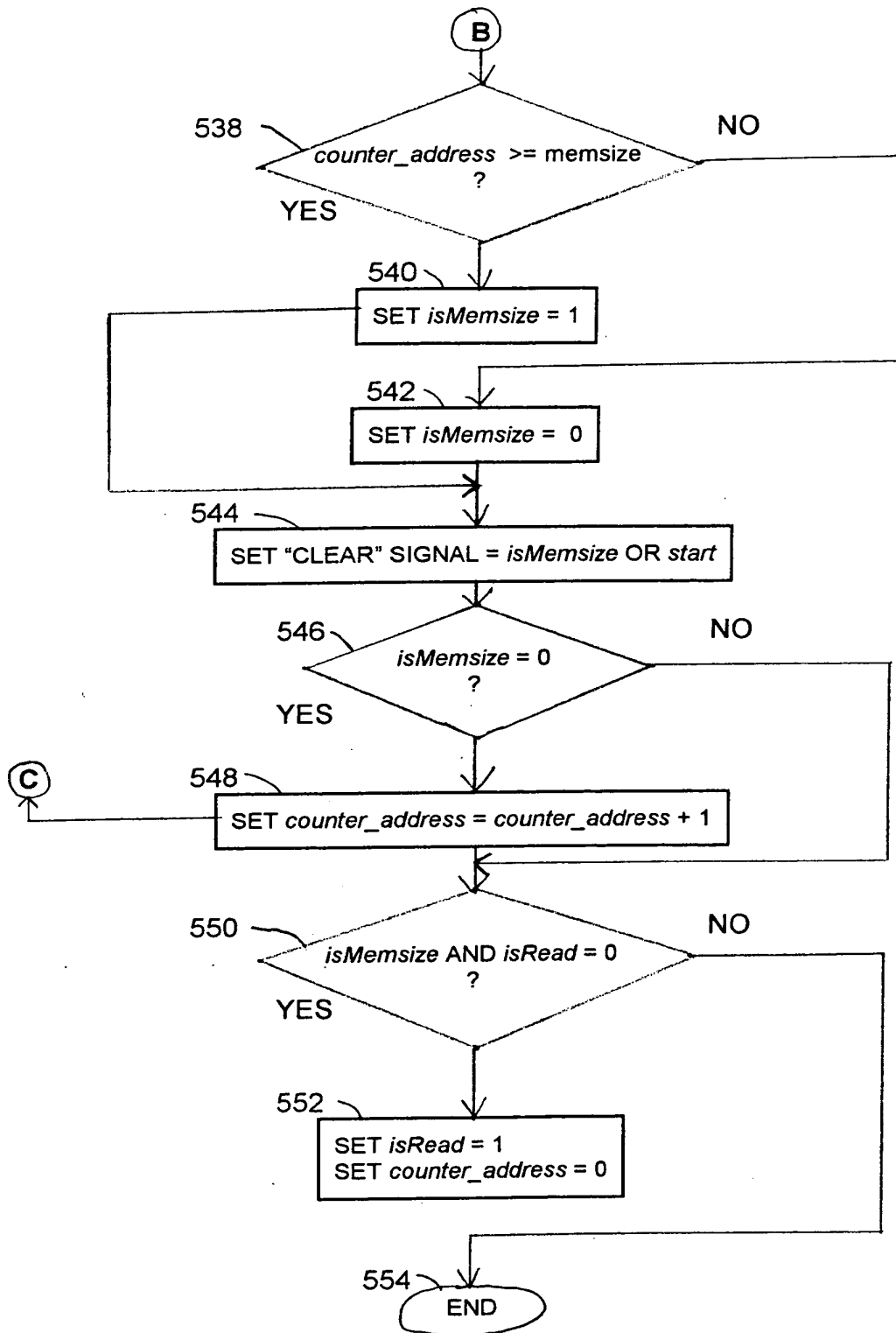


FIG. 5C

10027311 122101

	602		604		606	
	fill DR (n steps)		memory write (memsize steps)		memory read (memsize steps)	
clear	1 0 0 ...	0	0 0 ...	0	1 0 ...	0
move	1 1 1 ...	1	0 0 ...	0	0 0 ...	0
next	0 0 0 ...	0	1 1 ...	1	1 1 ...	1
we	0 0 0 ...	0	1 1 ...	1	0 0 ...	0
D	$\underbrace{0 \dots 0}_{m} \underbrace{1 \dots 1}_{m} \underbrace{0 \dots 0}_{m} \alpha$	$\alpha \oplus \text{isM}$	\dots	$\alpha \oplus \text{isM}$	$\alpha \oplus \text{isM}$	$\dots \alpha \oplus \text{isM}$

FIG. 6

	802		804		806		808	
	fill DR (n steps)		memory write $\text{memsize} + \lfloor \frac{\text{memsize}}{K} \rfloor$ steps		fill DR (n steps)		memory read $\text{memsize} + \lfloor \frac{\text{memsize}}{K} \rfloor$	
clear	1 0 0 0 ...	0	0	...	0	1 0 0 ...	0	0 ...
move	1 1 1 1 ...	1	0 ...	0 1 0 ...	0 1 ...	1 ...	1	0 ... 0 1 ...
next	0 0 0 0 ...	0	1 ...	1 0 1 ...	1 0 ...	0 ...	0	1 ... 1 0 1 ...
we	0 0 0 0 ...	0	1 ...	1 0 1 ...	1 0 ...	0 ...	0	0 ... 0
D	$\underbrace{0 1 0 1 \dots \bar{\alpha} \alpha}_n$	$\underbrace{\alpha \dots \alpha}_k$	$\underbrace{\bar{\alpha} \bar{\alpha} \dots \bar{\alpha}}_k$	$\underbrace{\alpha \dots \alpha}_k$	$\underbrace{0 1 0 1 \dots \alpha \alpha}_n$	$\underbrace{\alpha \dots \alpha}_k$	$\underbrace{\bar{\alpha} \bar{\alpha} \dots \bar{\alpha}}_k$	$\underbrace{\alpha \dots \alpha}_k$

FIG. 8

700

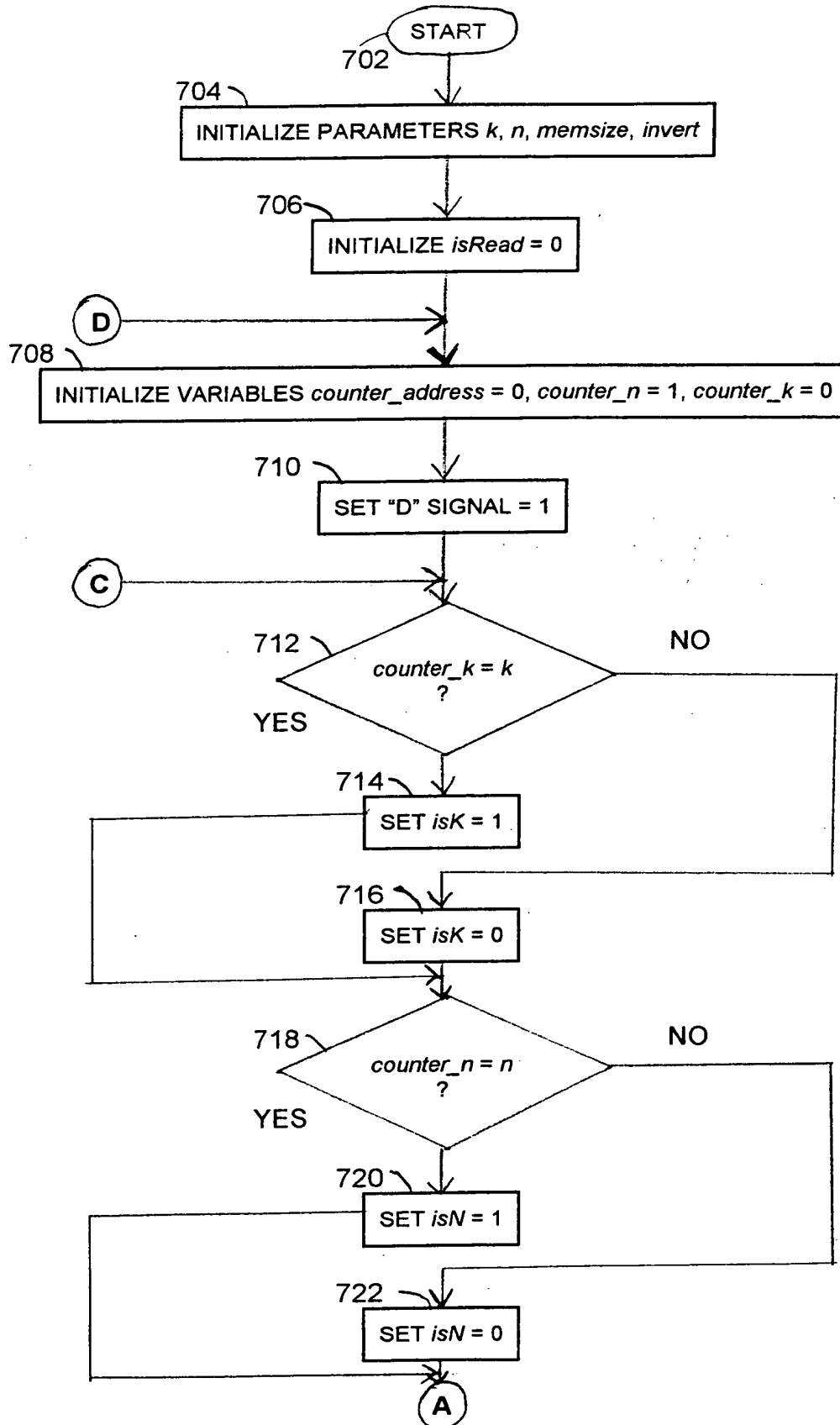


FIG. 7A

10027341-1001

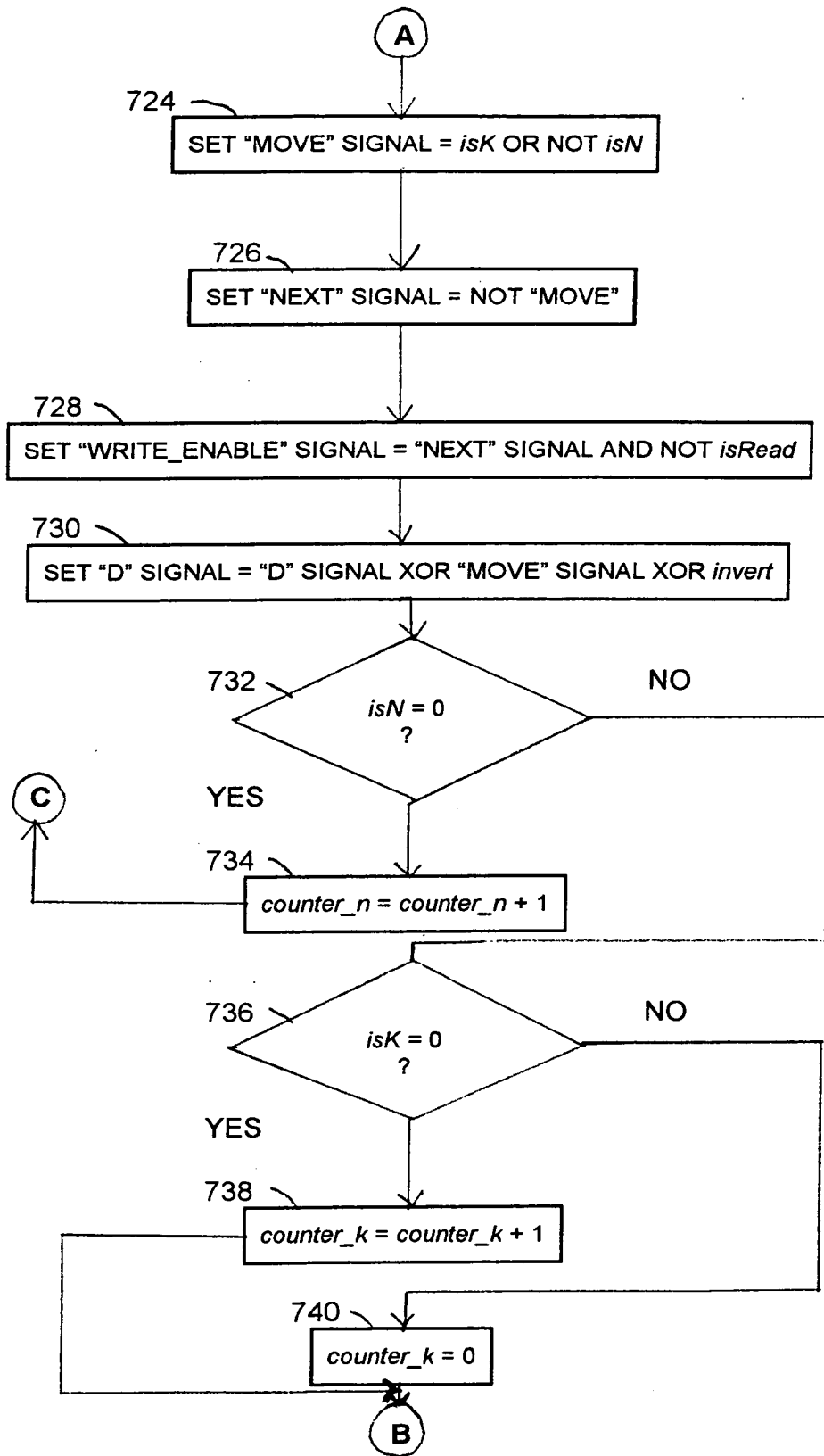


FIG. 7B

10027311-122101

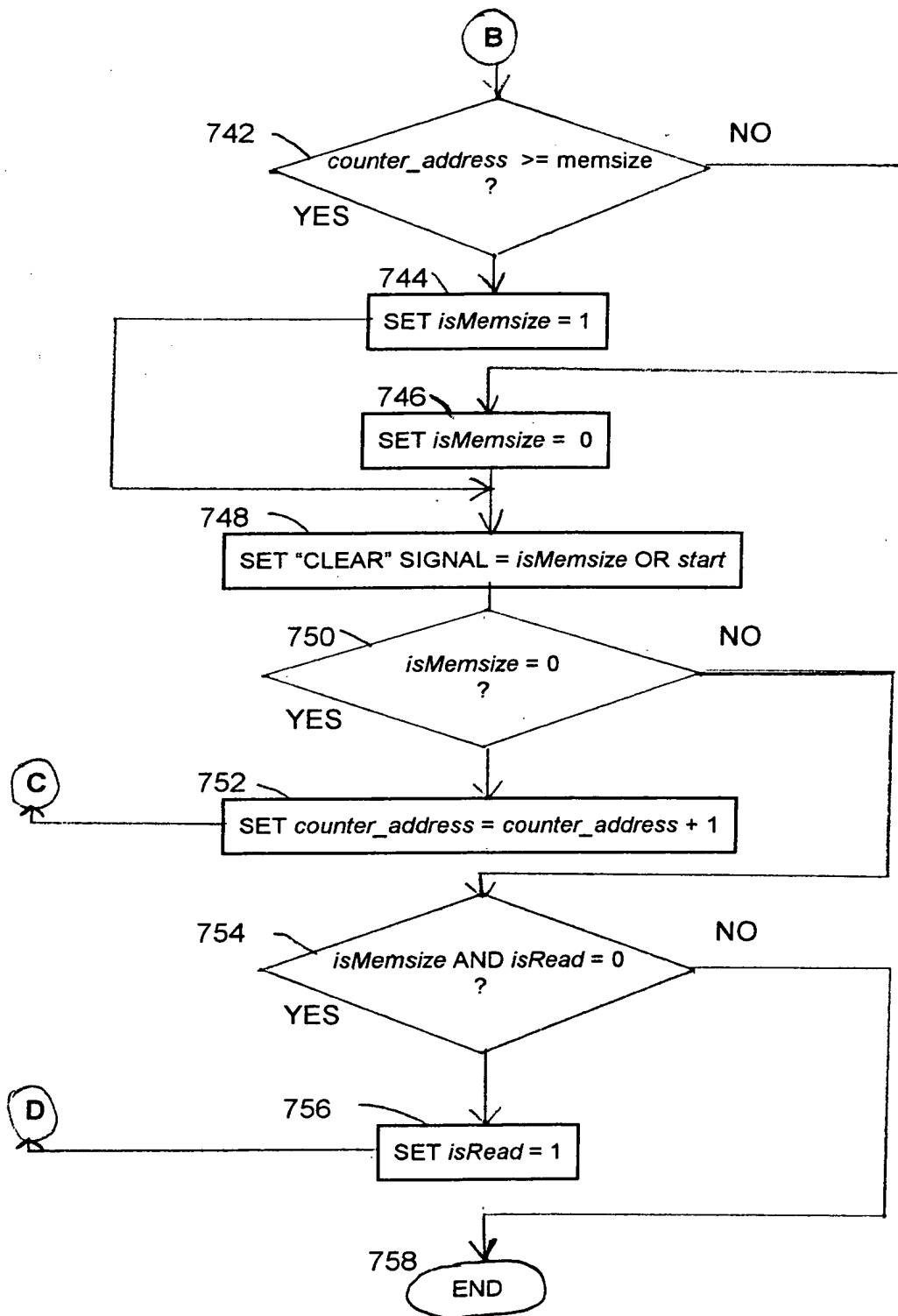


FIG. 7C